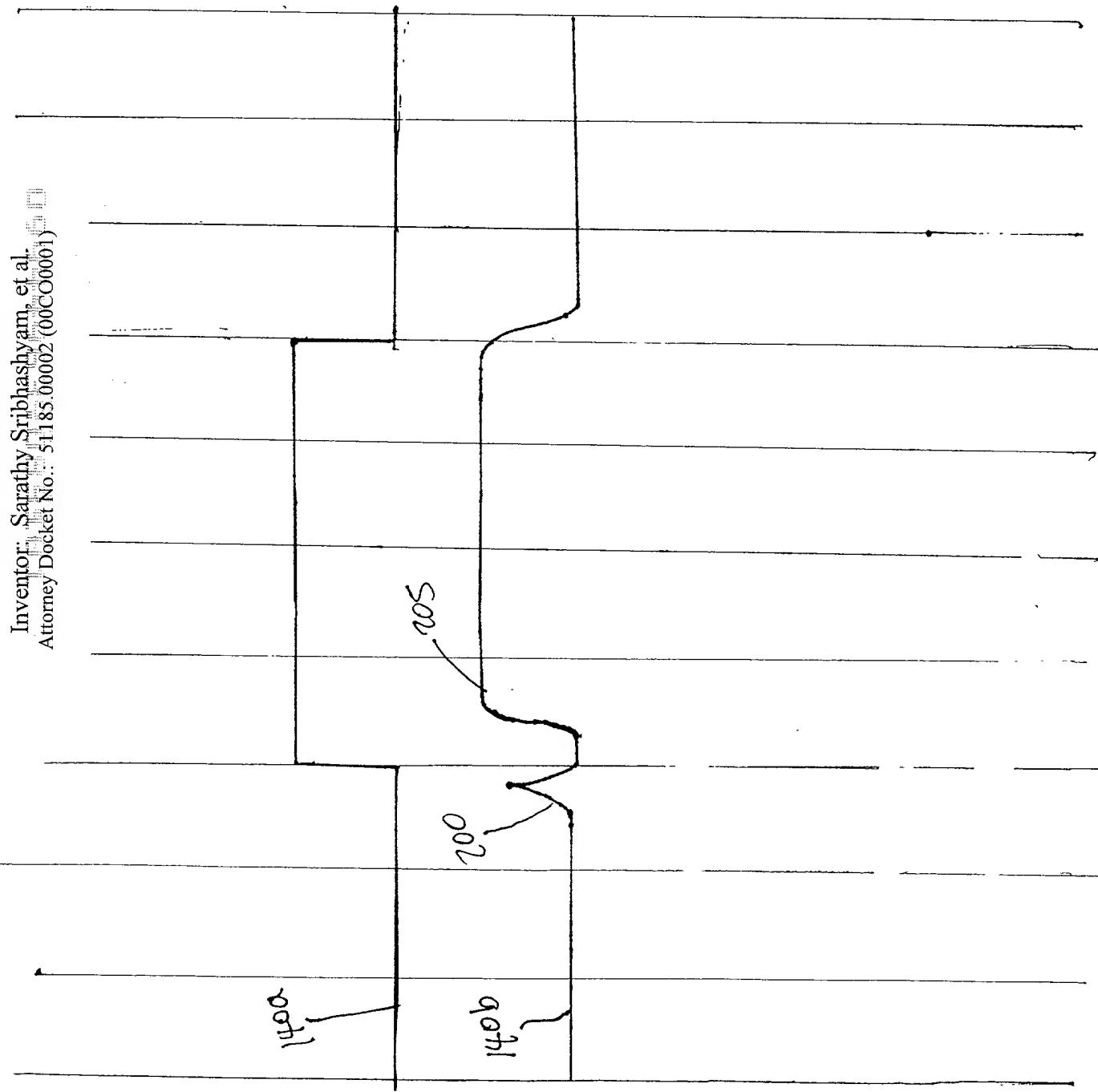


Figure 1
(prior Art)

Glitch Free Reset Circuit

Inventor: Sarathy Sribhashyam, et al.
Attorney Docket No.: 51185.00002 (00CO0001)

Figure 2



Glitch Free Reset Circuit

Inventor: Sarathy Sribhashyam, et al.
Attorney Docket No.: 51-185.00002 (00CO0001)

300 ✓

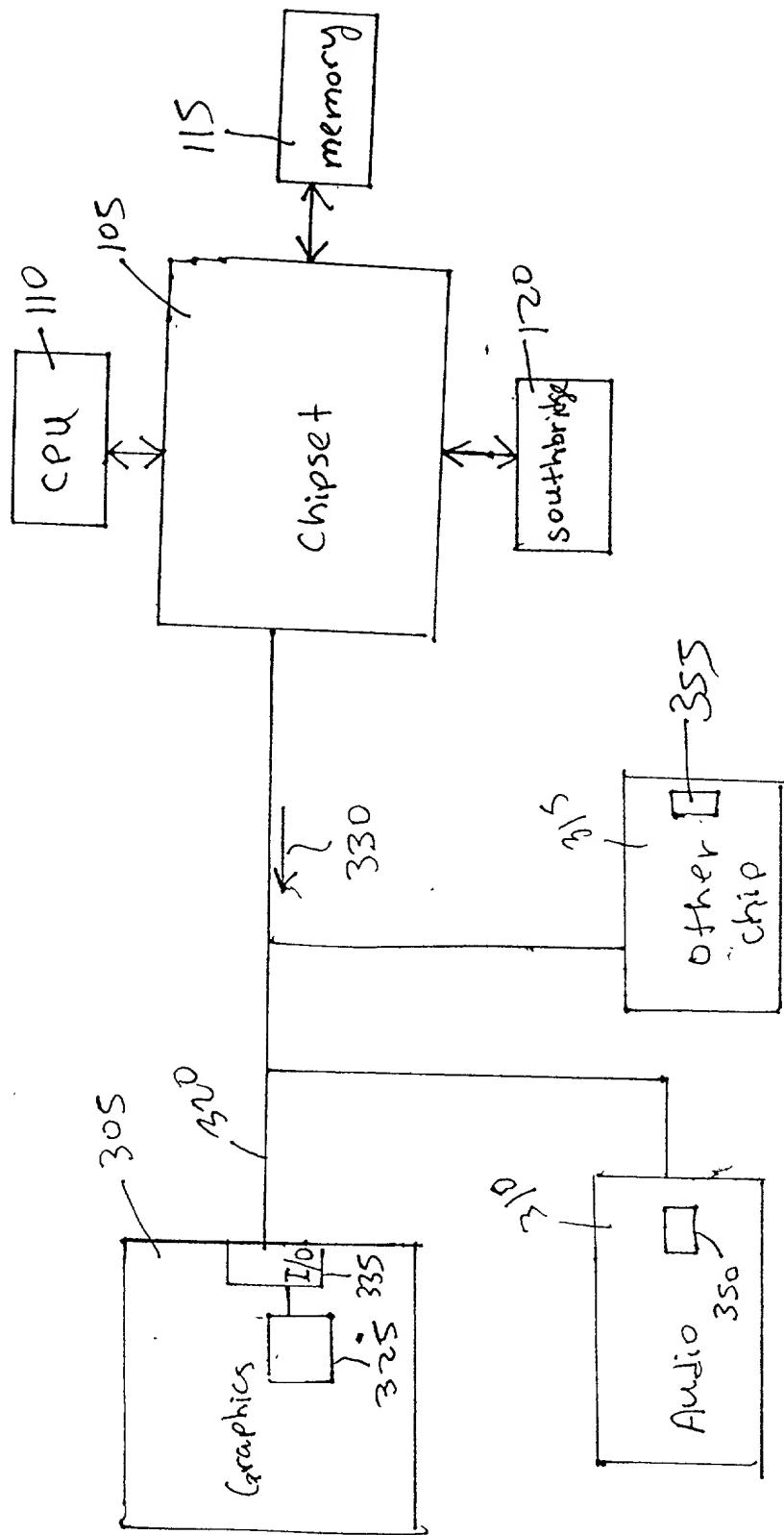


Figure 3

Glitch Free Reset Circuit

Inventor: Sarathy Sribhashyam, et al.
Attorney Docket No: 51185.00002 (00CO0001)

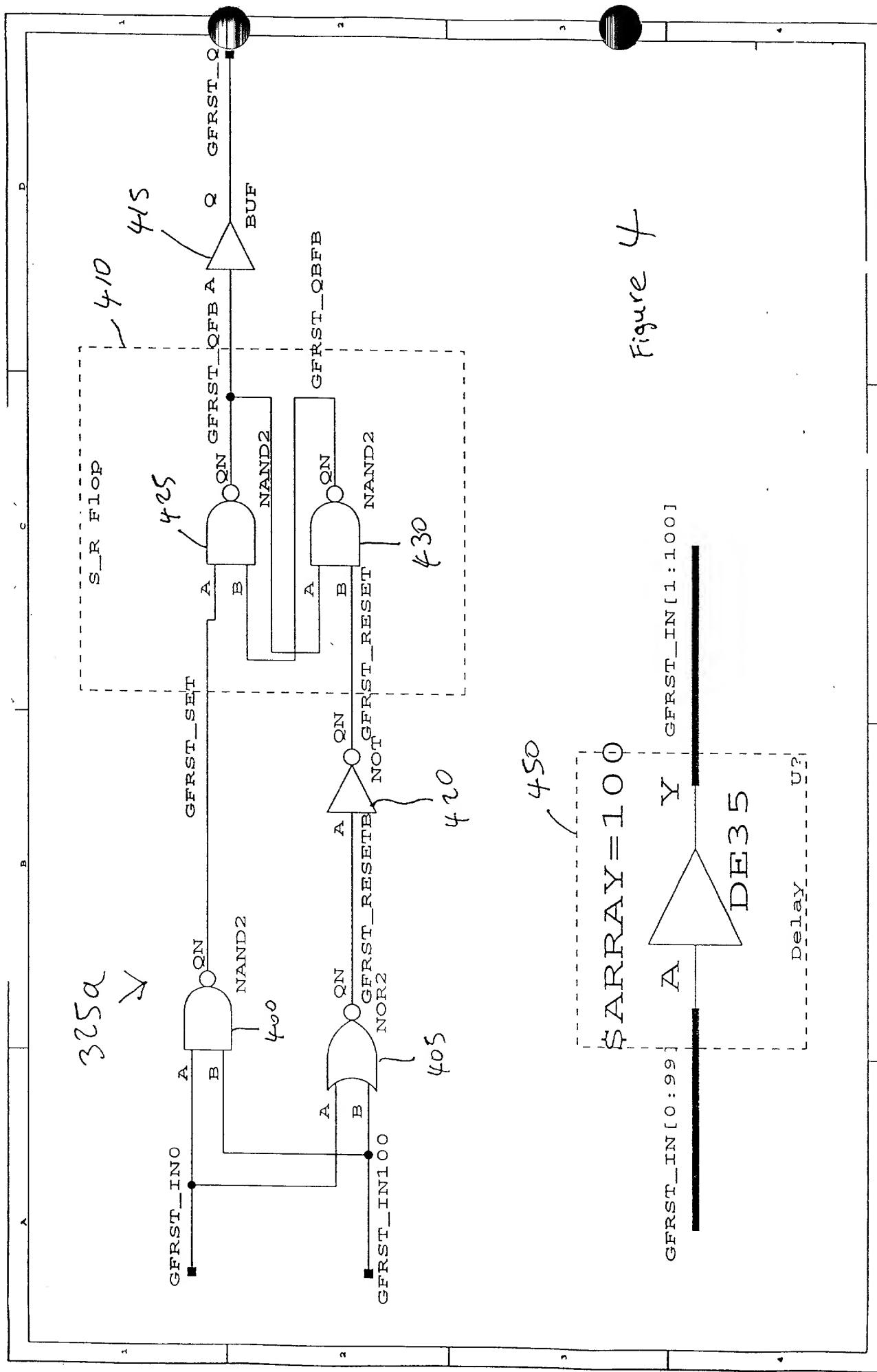


Figure 4

Glitch Free Reset Circuit

Inventor: Sarathy Sribhashyam, et al.
Attorney Docket No.: ST185.00002 (00CO0001)

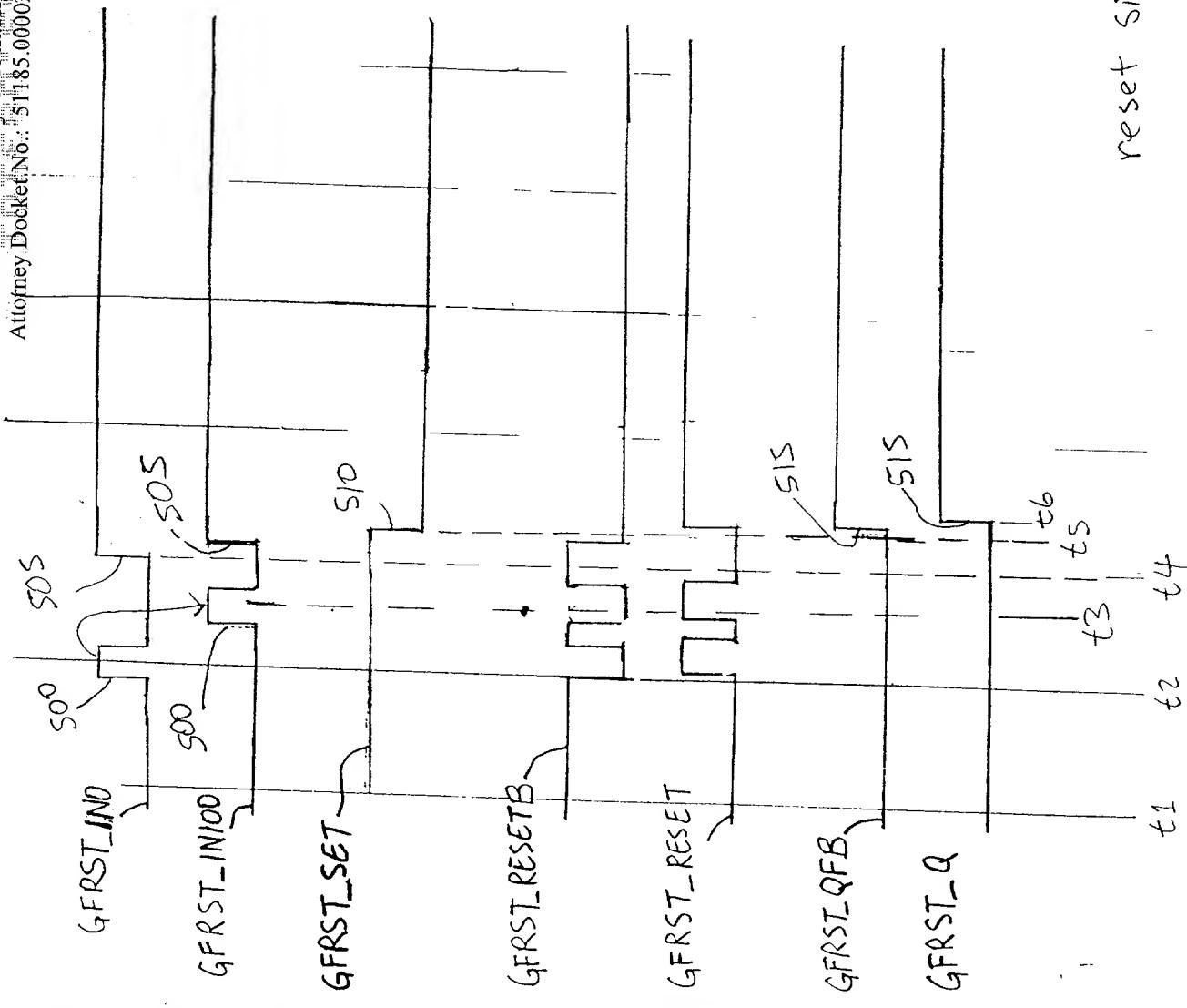


Figure 5

reset signal going high

Glitch Free Reset Circuit

Inventor: Sarathy Sribhashyam, et al.
Attorney Docket No.: 51185-00002 (00C00601)

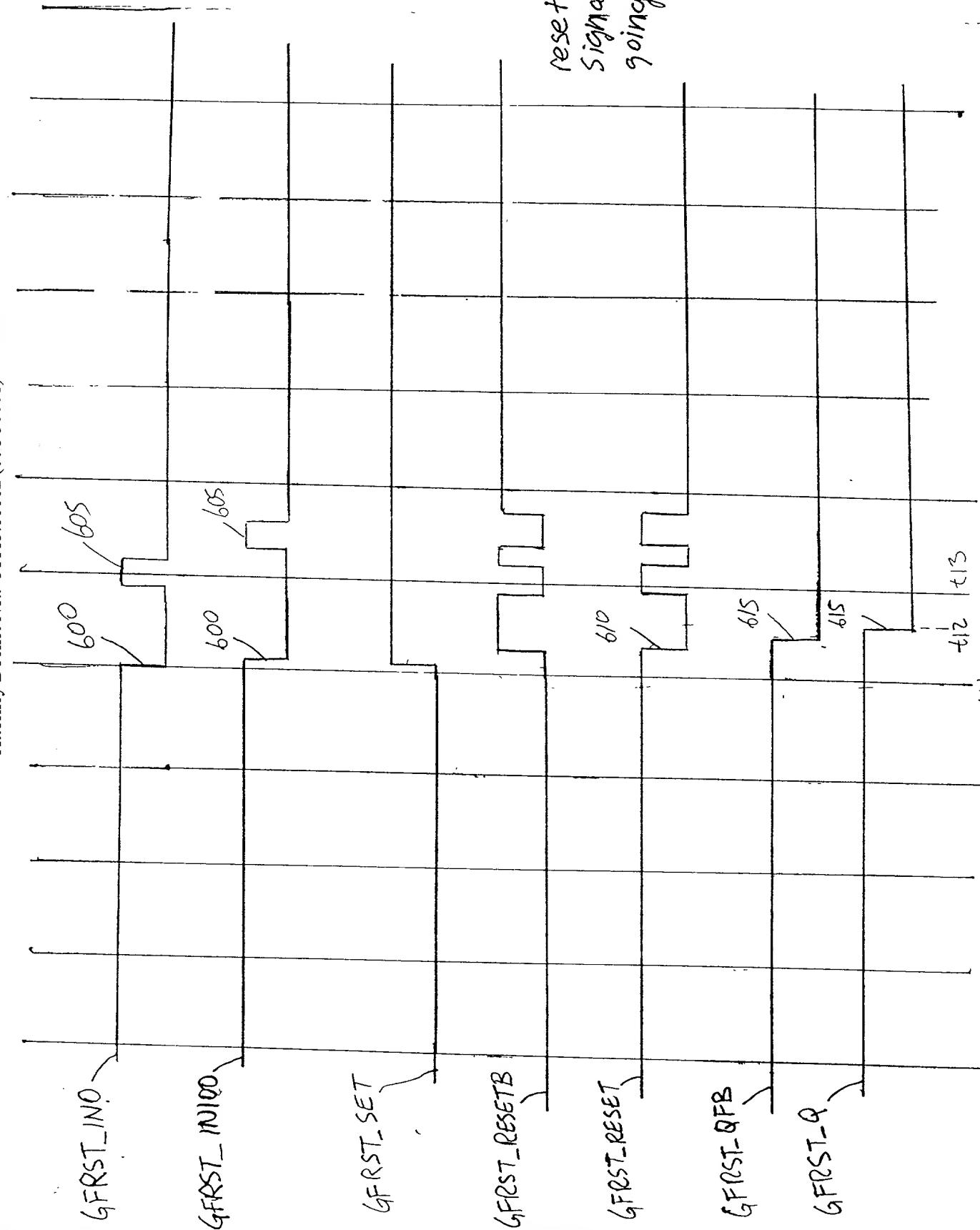


Figure 6

t_{10}

t_{11}

t_{12}

t_{13}

Glitch Free Reset Circuit

Inventor: Sarathy Sribhashyam, et al.
Attorney Docket No.: 51185.00002 (00CC0000)

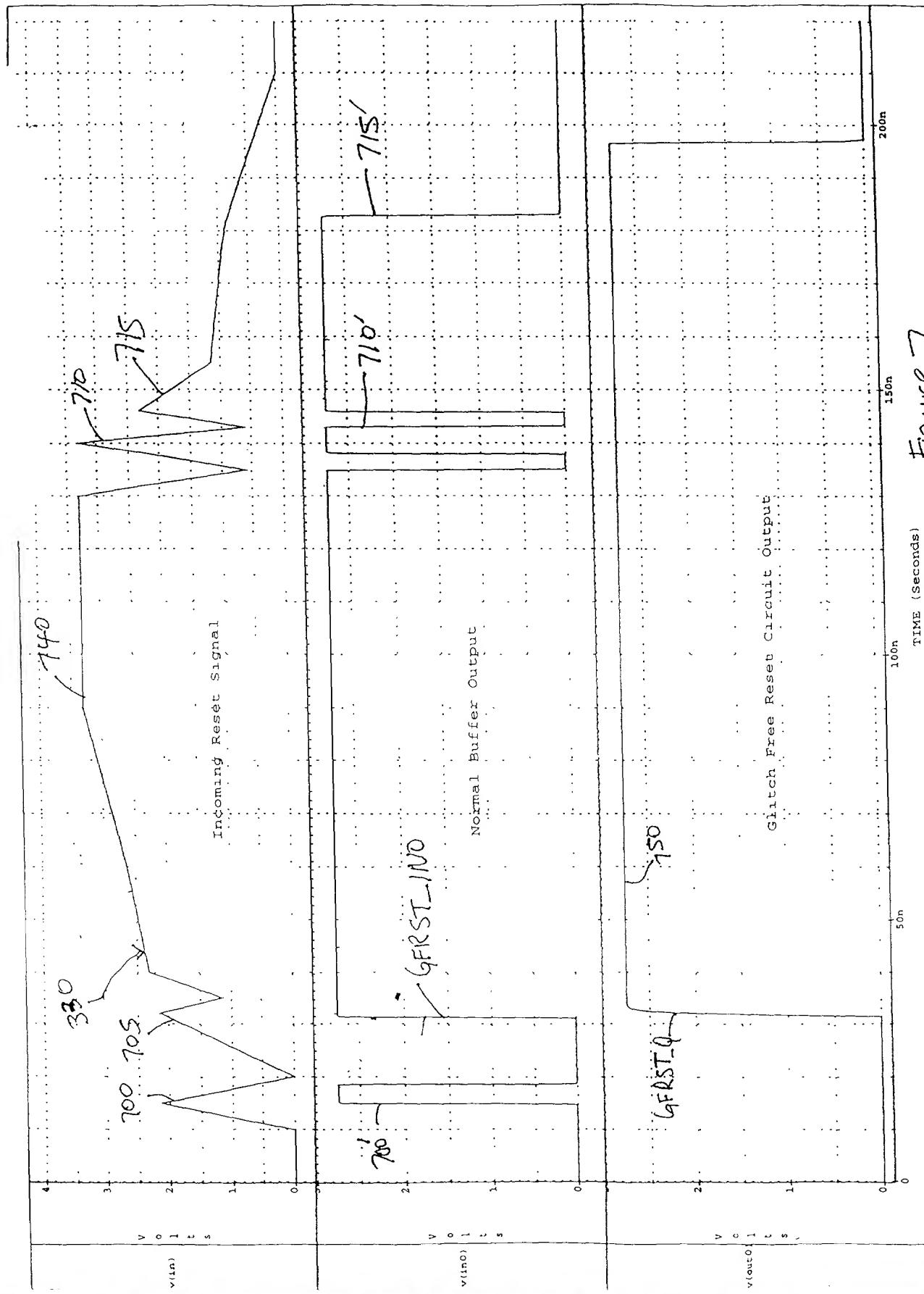
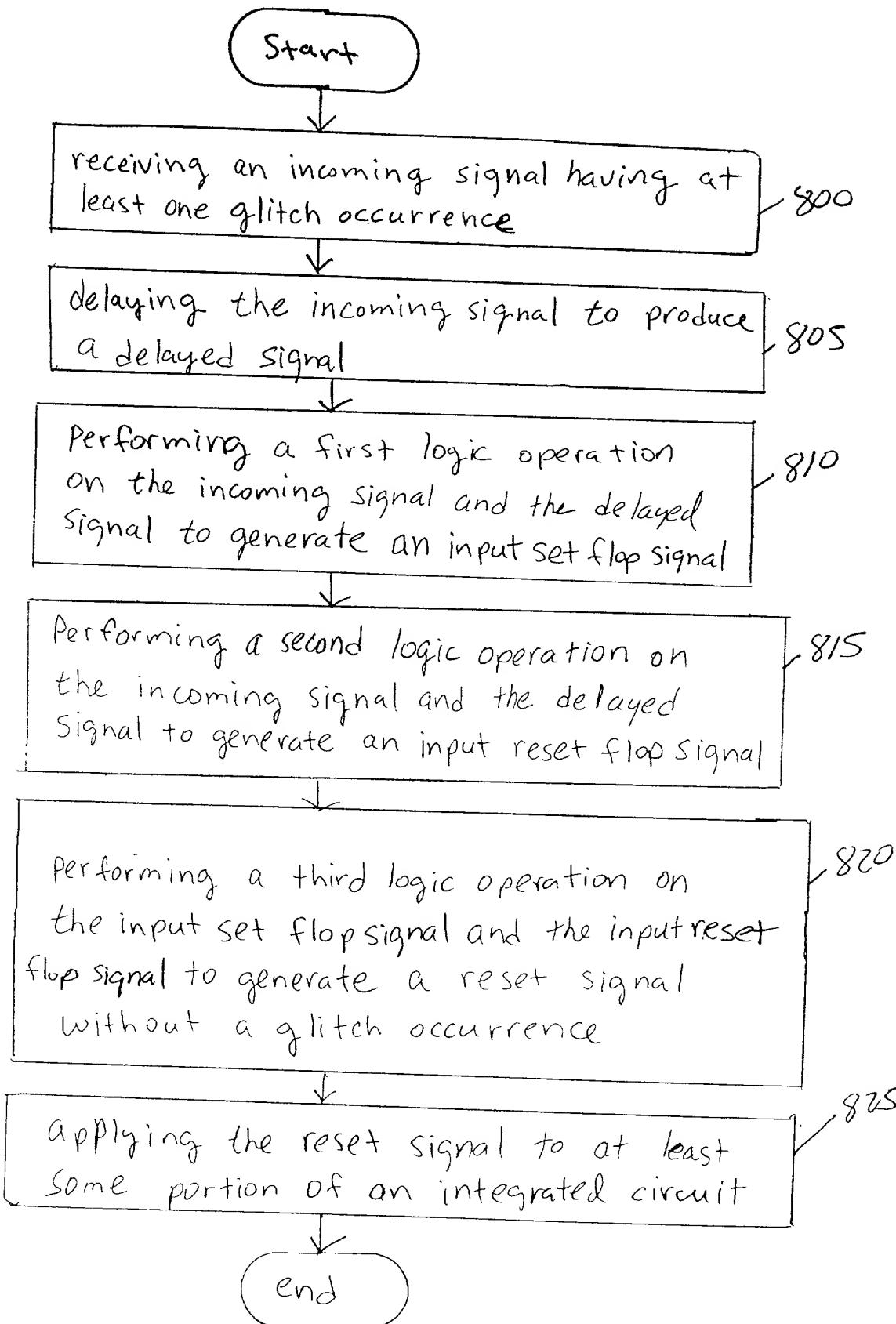


Figure 7

Glitch Free Reset Circuit

Inventor: Sarathy Sribhashyam, et al.
Attorney Docket No.: 51185.00002 (00CO0001)



Method of compensating for glitch occurrence

Figure 8

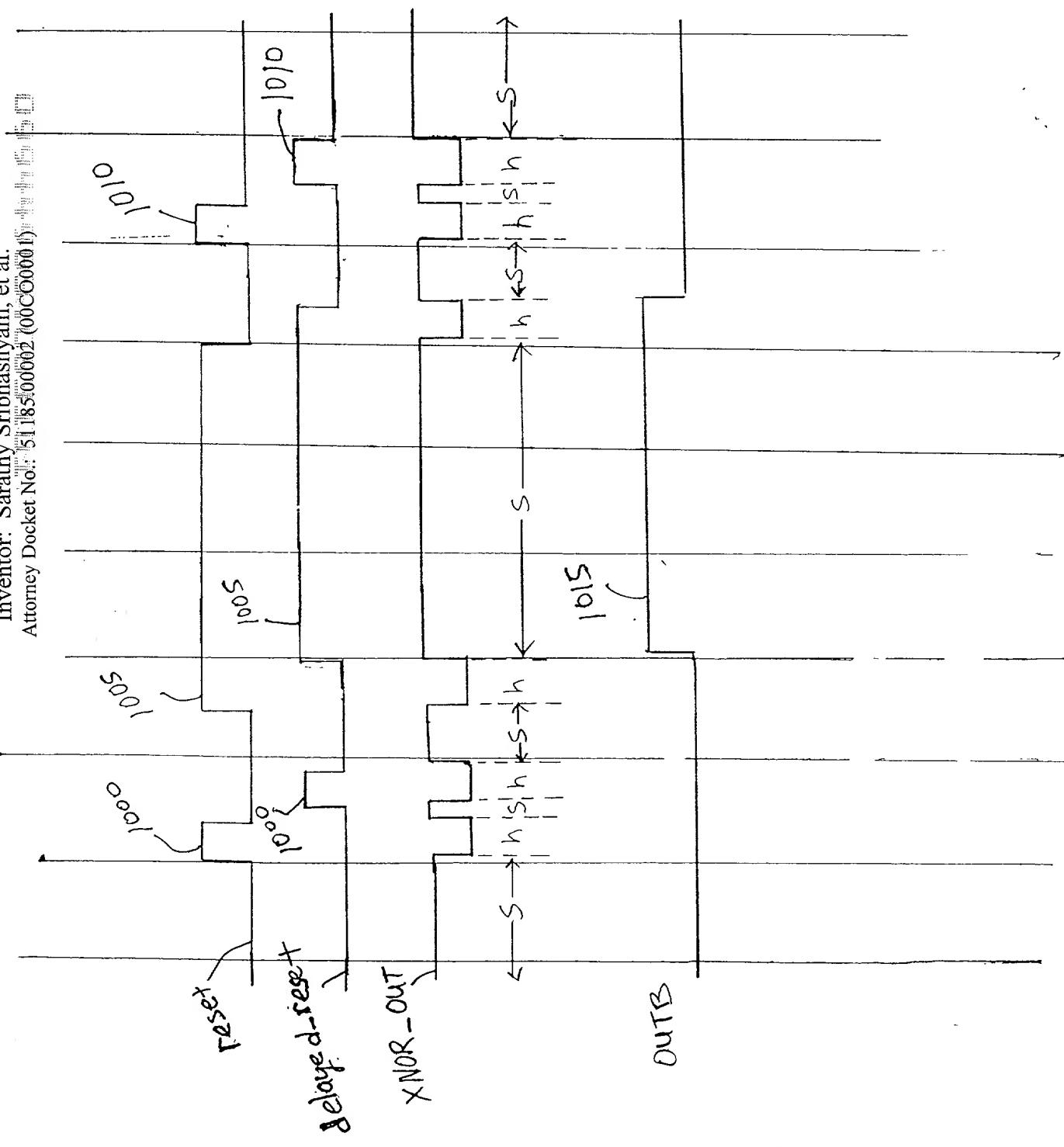


Figure 10

Glitch Free Reset Circuit

Inventor: Sarathy Sribhashyam, et al
Attorney Docket No.: 51185.00002 (00CO0011)

